

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	09/603,147
Filing Date	
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	2813
Examiner	Kielin, Erik J.
Attorney's Docket No	MI22-1443
Title: Capacitor Constructions, DRAM Construc	tions and Semiconductive
Material Assemblies	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To:

Mail Stop RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

Dear Sir:

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated:

By:

Jennifer J. Taylor, P.

Reg. N/6. 48,7

Form PTO-1449 U.S. DEPART PATENT AND			DEPARTME	NT OF COMMERCE ADEMARK OFFICE	ATTY. DOCKET N MI22-1443	ATTY. DOCKET NO. MI22-1443				SERIAL NO. 09/603,147		
						APPLICANT John T. Moore et al.						
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U.S. PATENT DOCUMENTS												
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	AR		John L. Vossen, et	at., "Thin Film	Processes", A	Academic Press, 1978,	p. 290					
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EXAMINER	EXAMINER DATE CONSIDERED											
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